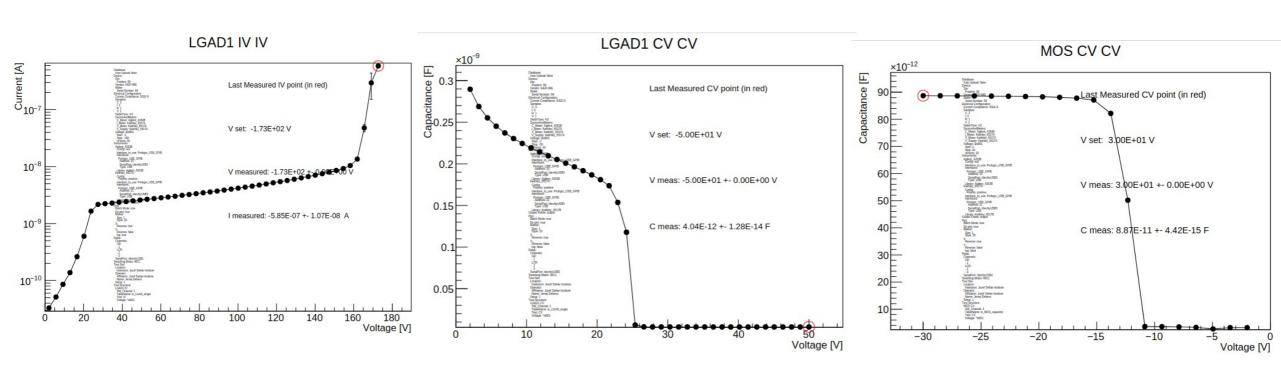
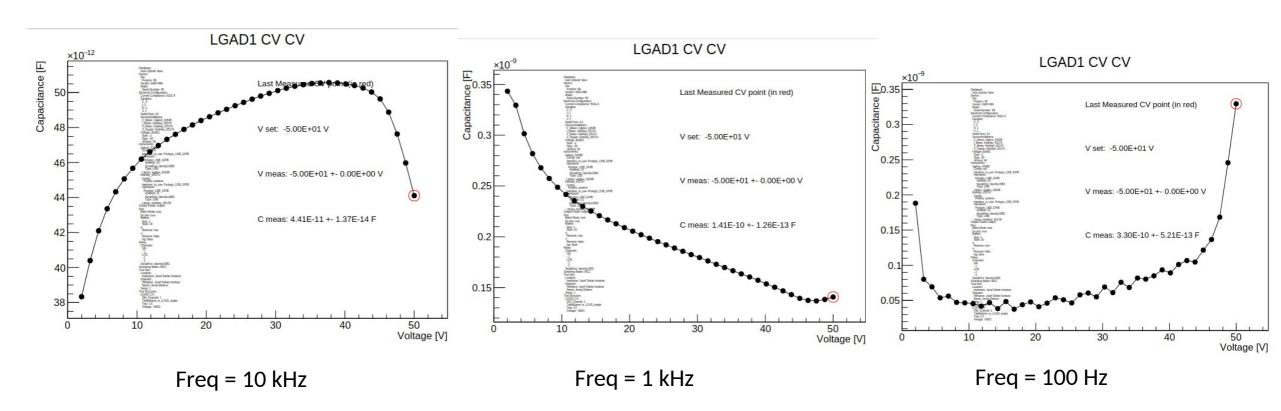




## CV frequency tests for irradiated samples





## Setup configuration:

• All GRs (LGADs, PIN, MOS, VDP) shorted together on the inside of the enclosure

